

<b>Notice of References Cited</b>	Application/Control No. 10/632,930	Applicant(s)/Patent Under Reexamination FUKUNAGA, TAKESHI	
	Examiner William M. Brewster	Art Unit 2823	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
X	A	US- 6,602,761 B1	08-2003	FUKUNAGA	438/459
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stanley Wolf Ph.D. in Silicon Processing for the VLSI Era, Volume 2: Process Integration, Lattice Press, 1990, pp. 238-9.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.